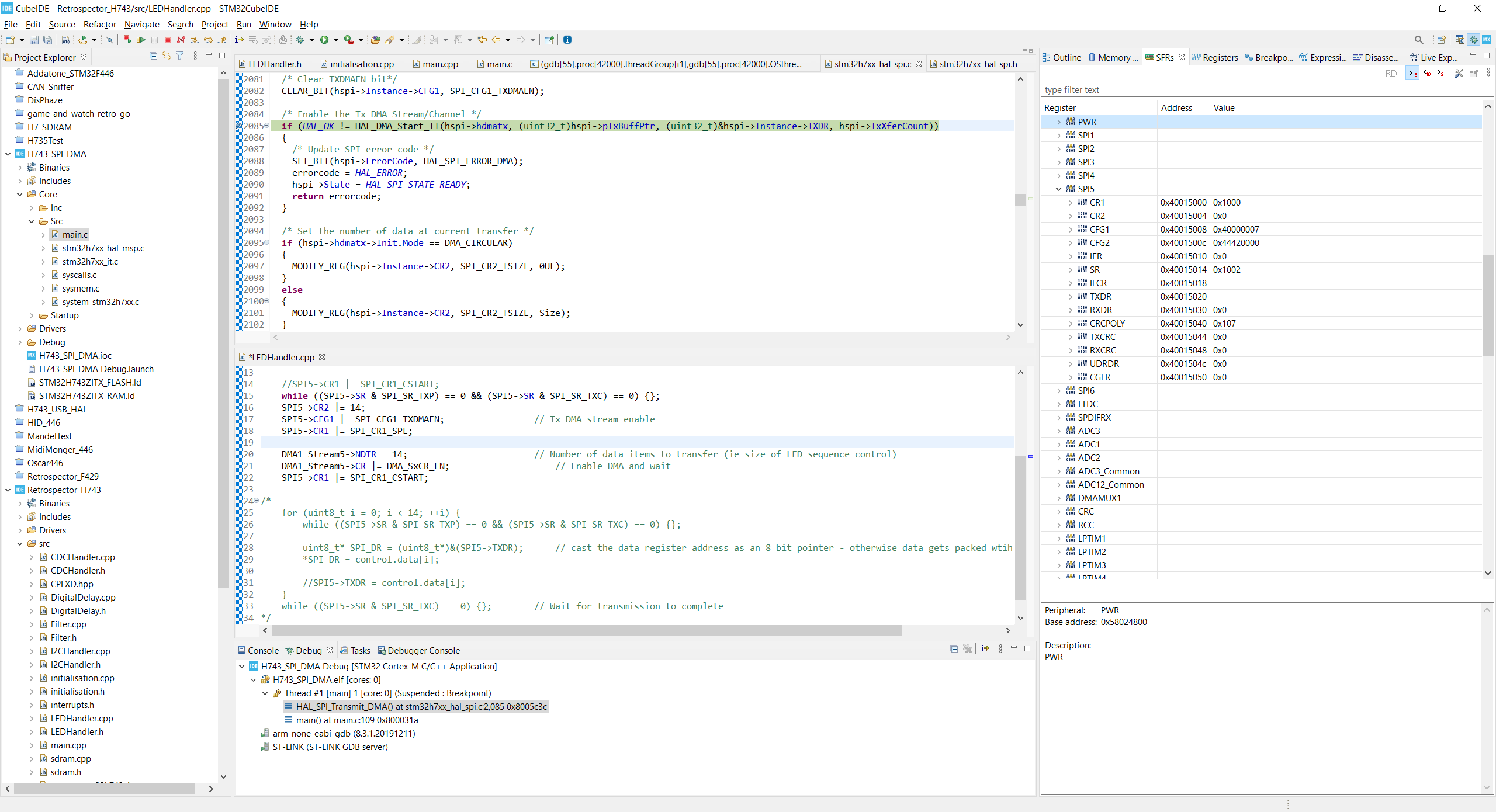
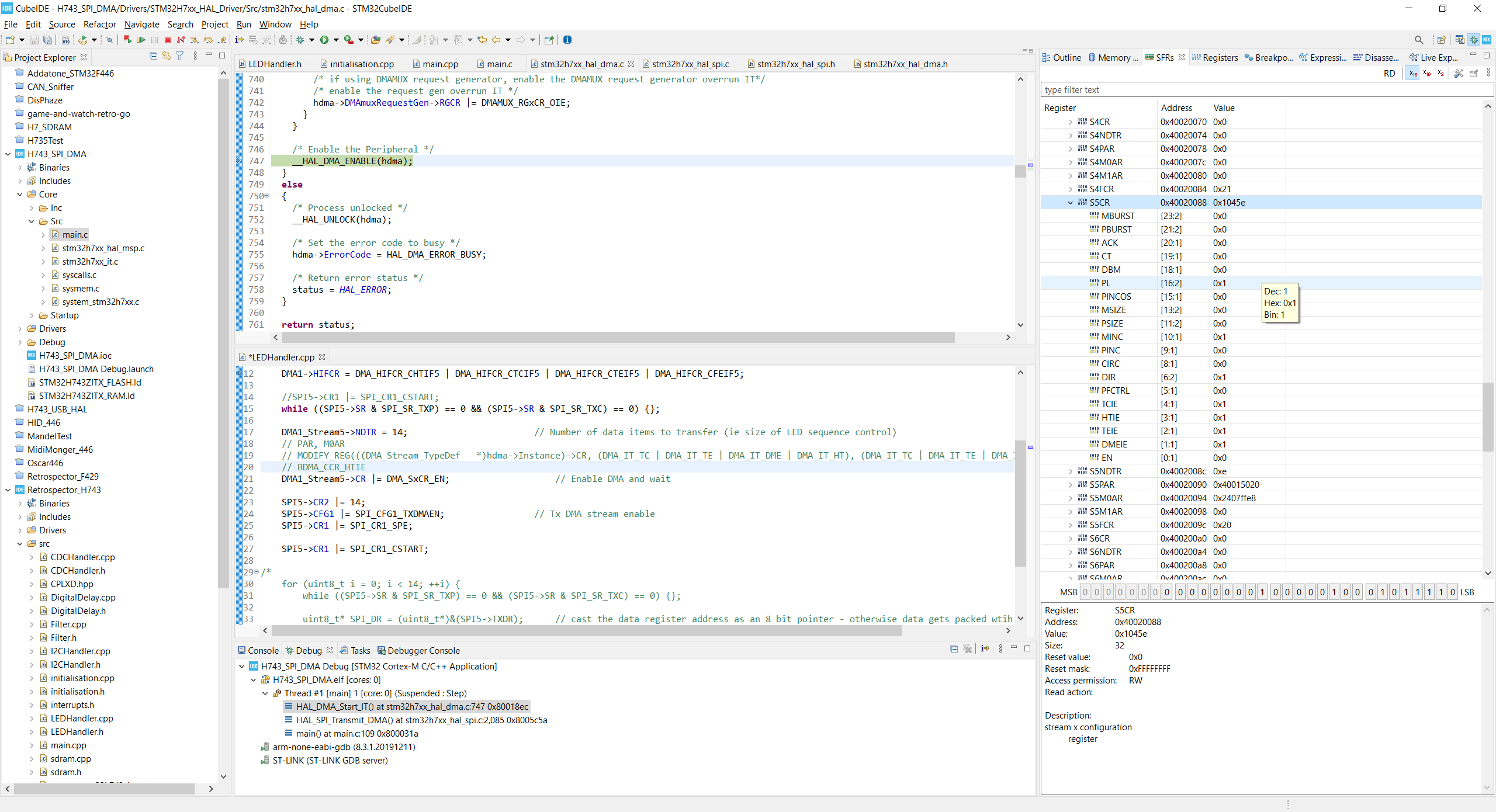


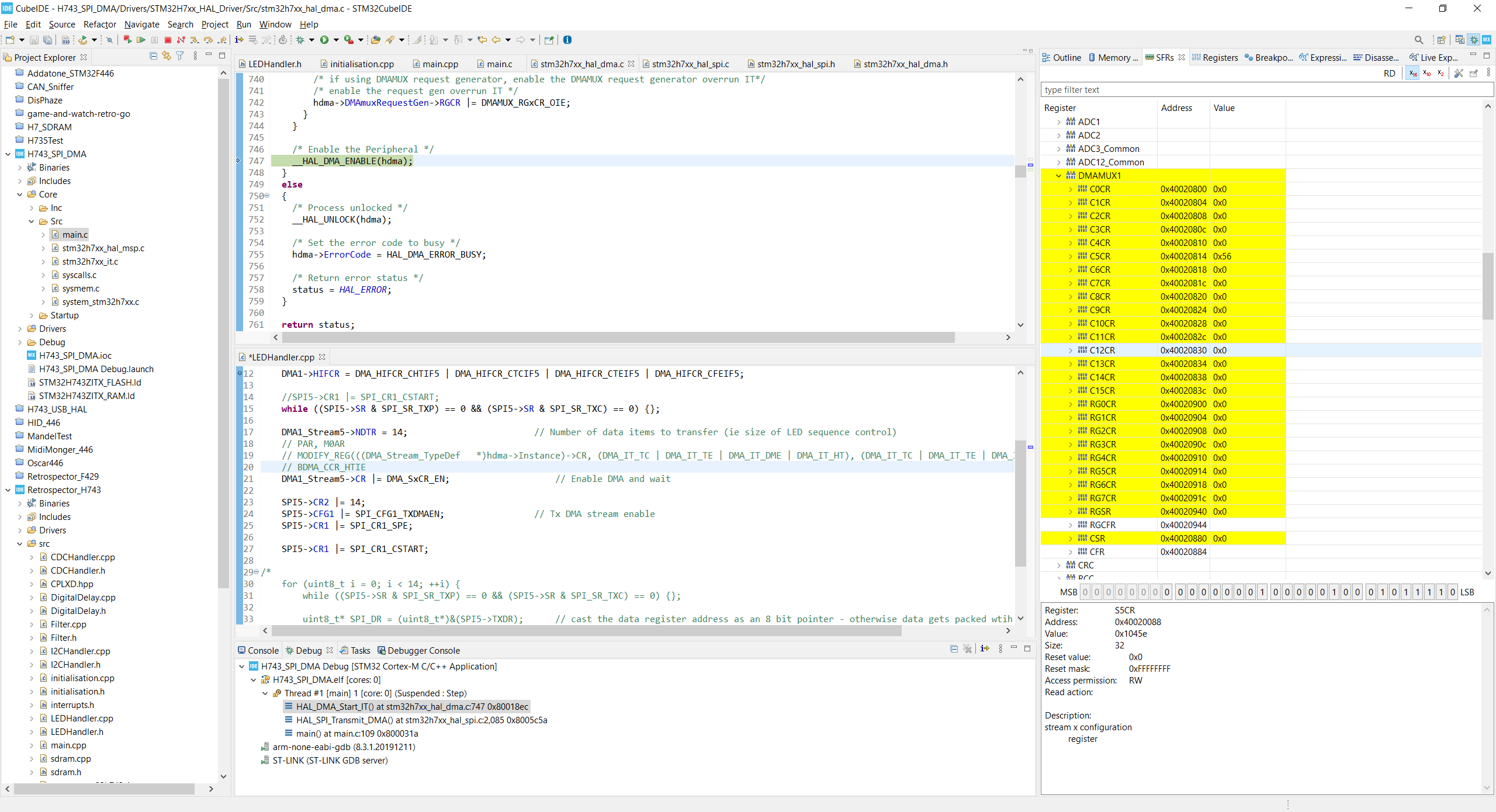
Pre DMA:



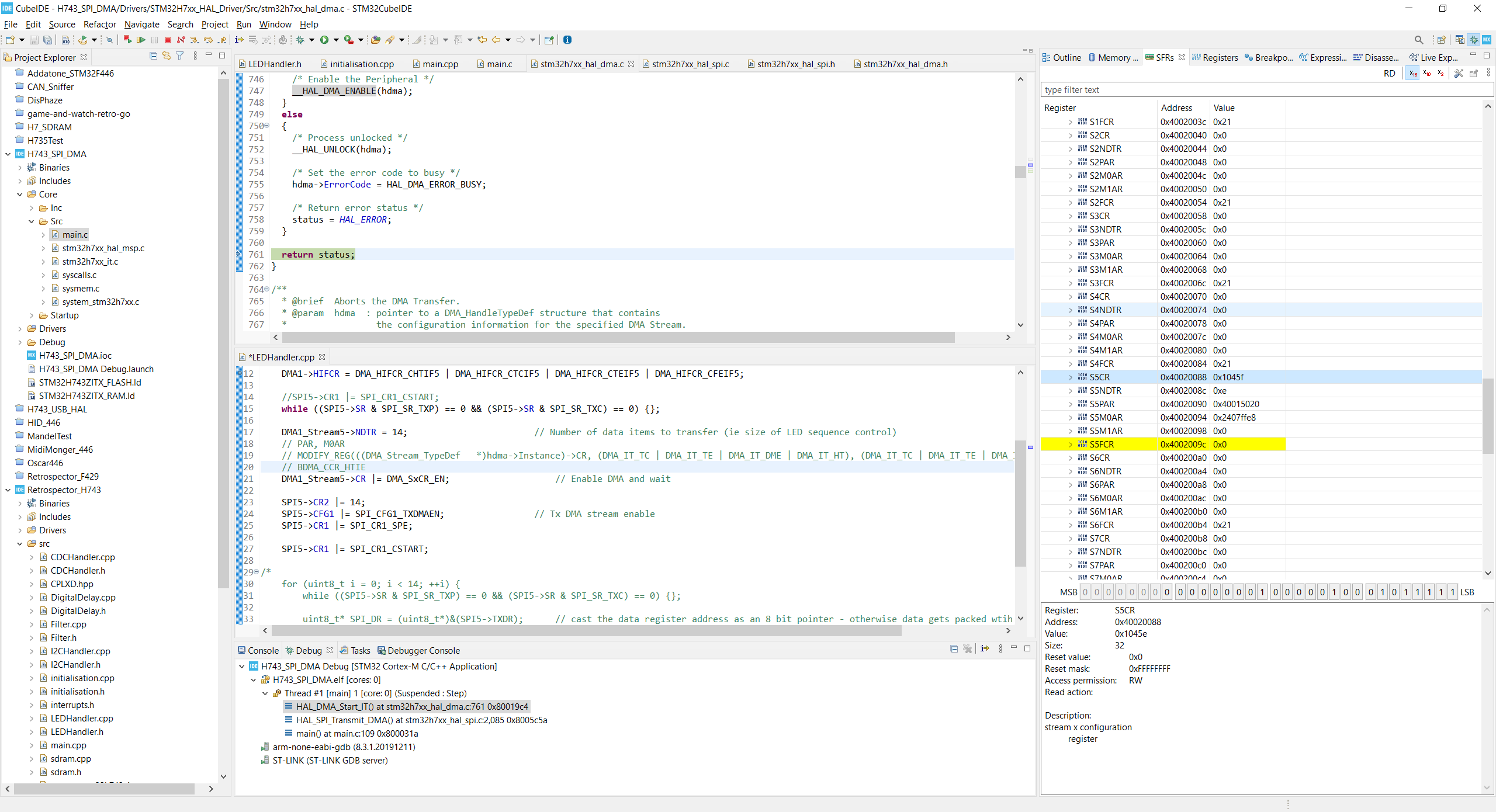
Clear CFG1 CRC SIze

DMA Pre enable:

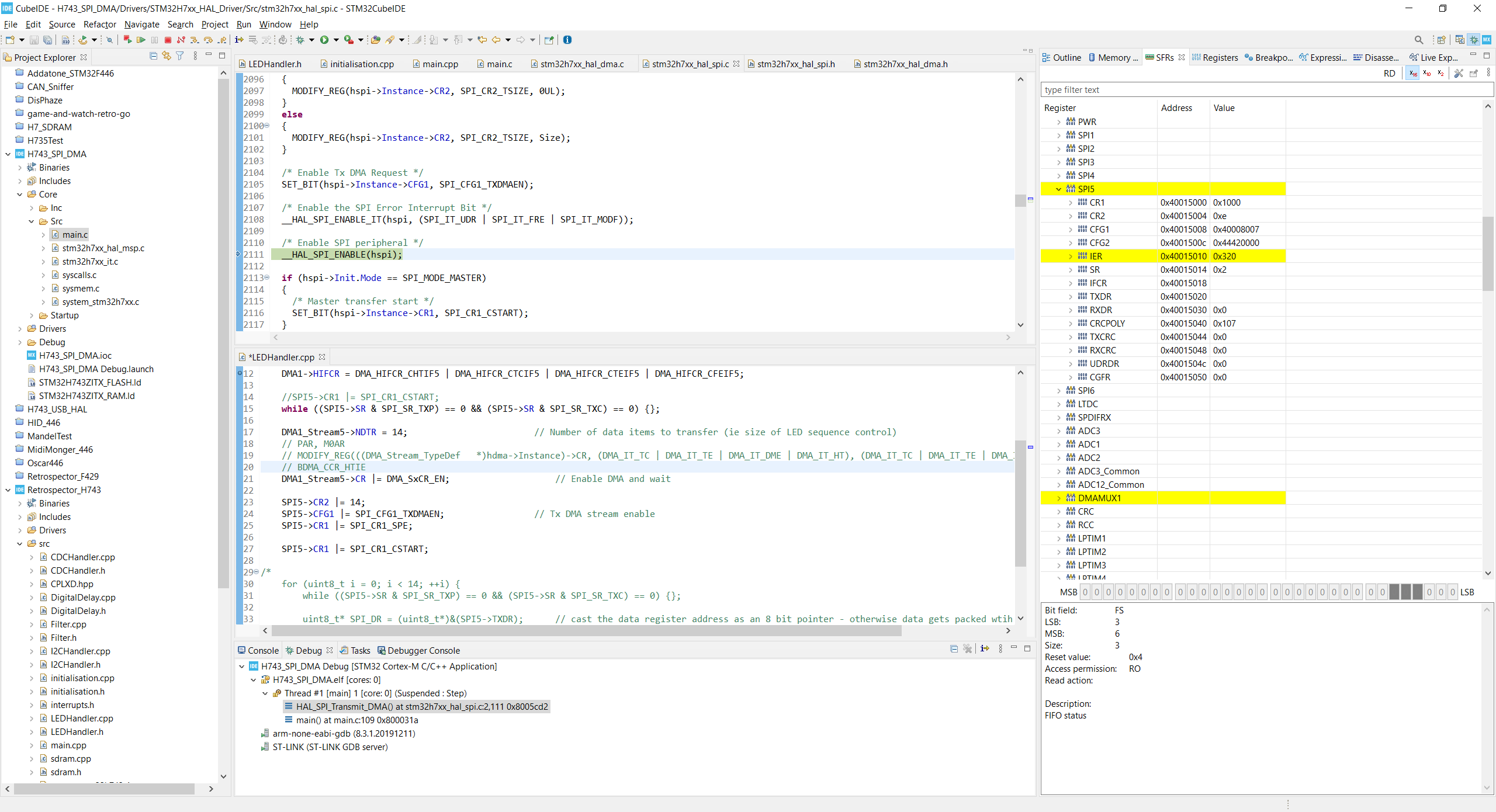




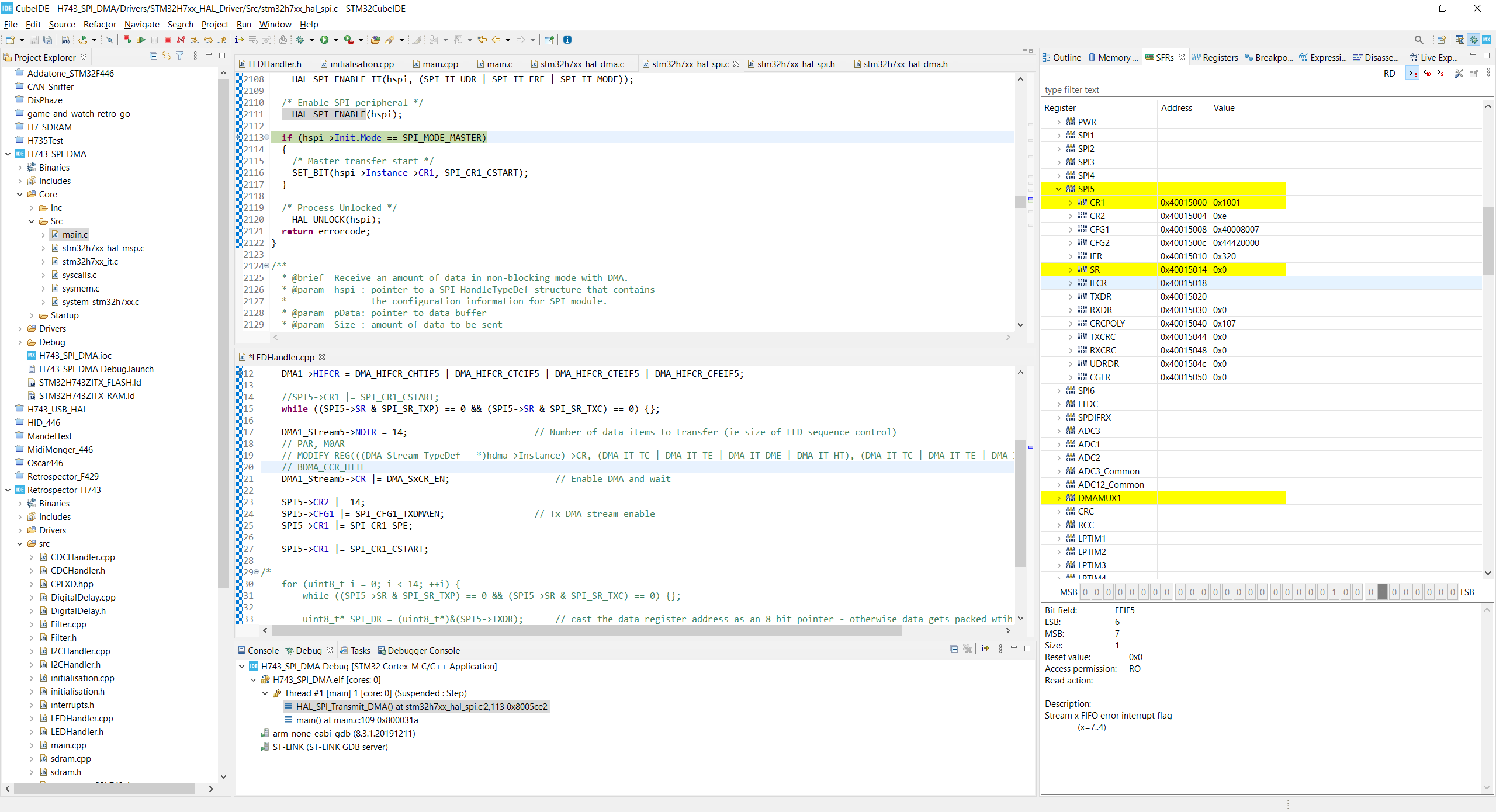
Post DMA enable:

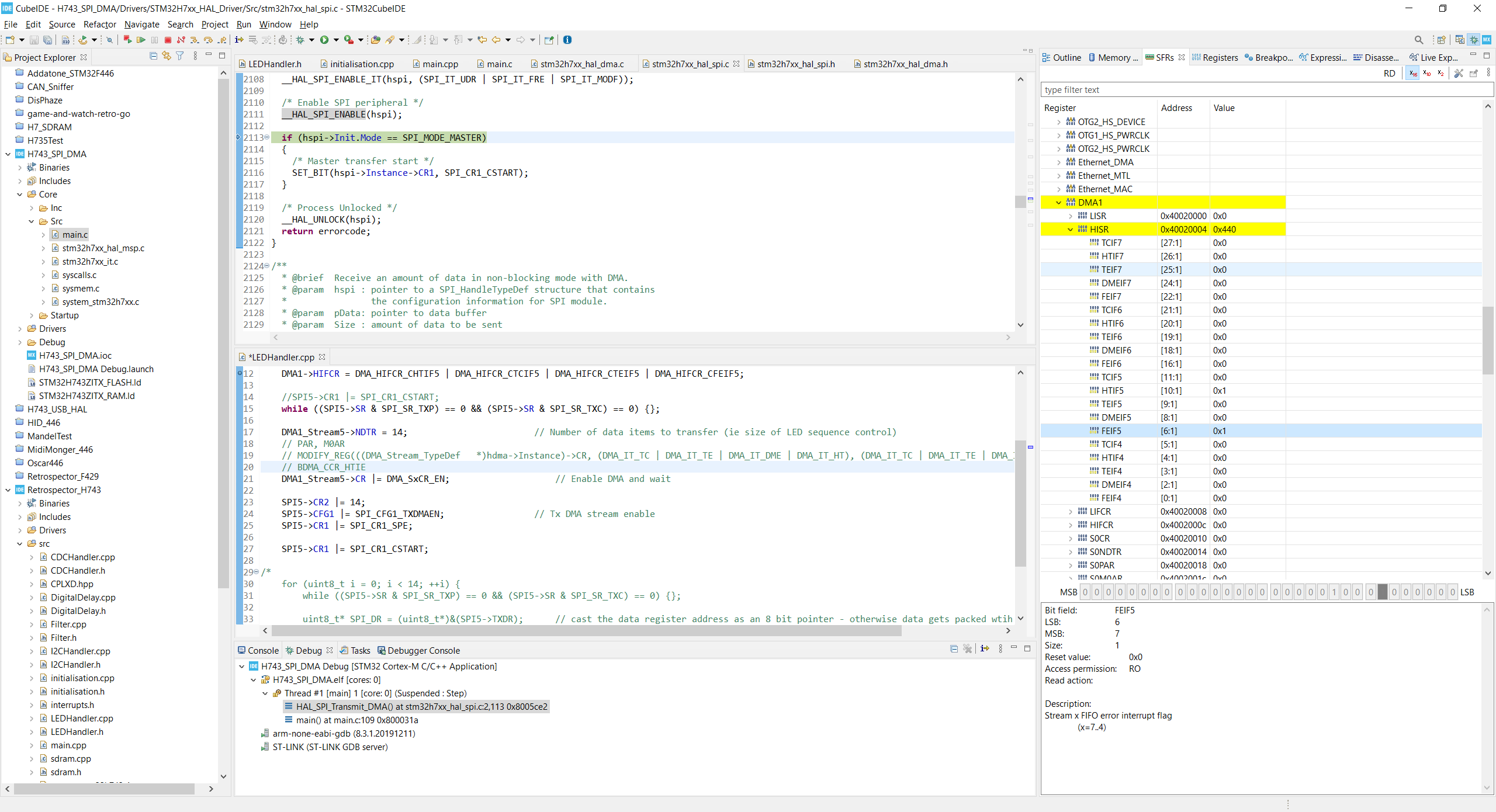


Pre SPI enable:



Post SPI Enable:





Post run:

